



SIPE^ARL

**No European digital sovereignty
without sovereign hardware**

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Riser Workshop #3

December 9, 2025



European
IC design house



SiPearl, a startup funded by Europe & France

To provide sovereign high-performance energy-efficient processors for Europe's supercomputers, AI companies and data centres



Incorporated

In France in June 2019, deployed in Spain, Italy



Seed funded

By the European Union



Funded by the European Union



Financing

Series-A: €130m



國泰創投

European Innovation Council Fund



EU



EVIDEN FRANCE 2030



Fabless IC design house

Manufacturing entrusted to TSMC with N6P



Sovereign design

Data centre in France

317 servers / 55 networks

48 storage / 5 backup server



200 employees

from **Atos** **MEDIATEK** **ST** **MARVELL**
NXP **Hewlett Packard Enterprise** **intel.** **NOKIA**



2 design wins

JUPITER & Alice Recoque, 2 European exascale supercomputers funded by EuroHPC and EU Member States

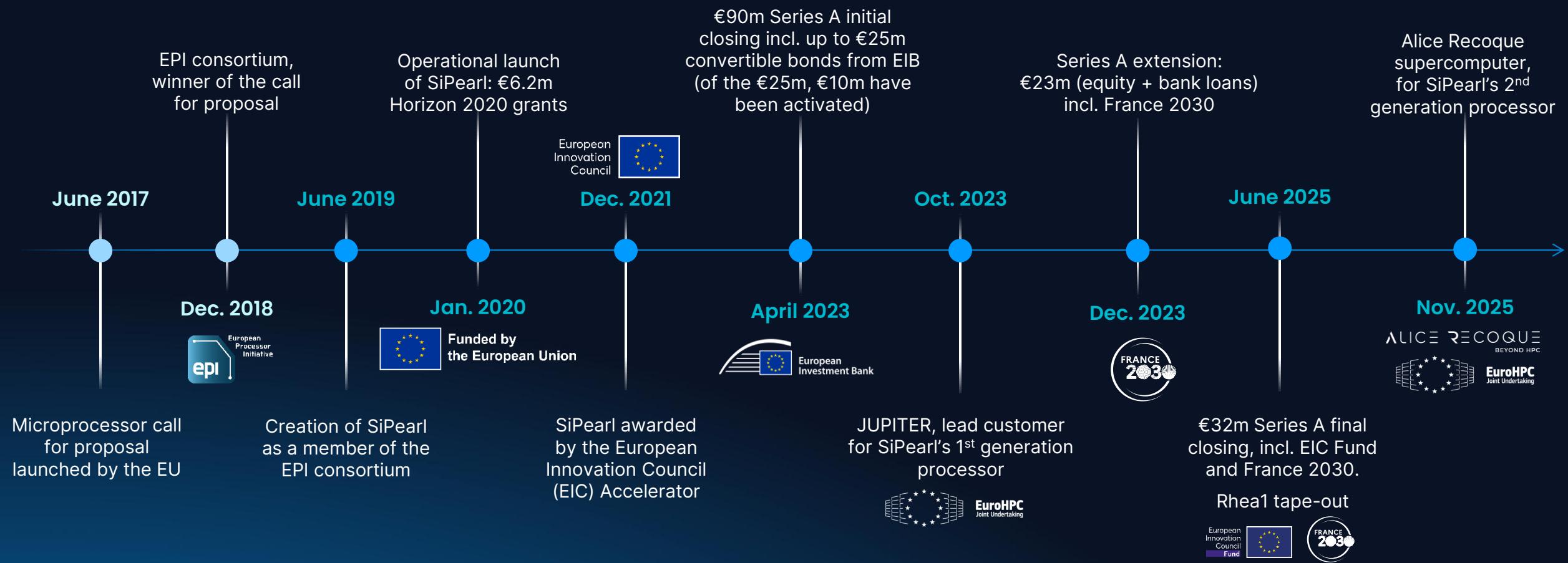


1st tape-out

June 2025

From a European Union concern to SiPearl's ramp-up

Our common goal: fostering the return of high-performance, energy-efficient processor technologies in Europe.



SiPearl's role within the Riser project

Hardware Platform Prototyping (WP3)

- Hardware and low-level software integration of the PCIe acceleration board in Arm-based servers

Firmware & System Software (WP4) and use cases (WP5)

- Based on our server development experience, contribution to the development and validation of boot support on accelerator and microserver boards
- Contribution to the development of host-side drivers for Arm-based servers to enable interaction with accelerator board compute resources
- Contribution to the definition of the outcome of the three use cases as defined in WP5

Equipment

- 2 Rhea1 servers (Seine servers) for the acceleration use case, when combined with Risc-V PCIe acceleration boards

For memo

SiPearl is leader on:

- T2.1: Requirements & use cases refinement
- T6.2: Dissemination & communication
- D2.1: Requirements & use cases deliverable
- D6.2 + D6.3: Initial + Final report on Dissemination & Communication deliverables



No European digital
sovereignty without
sovereign hardware



Europe: Digital sovereignty has become a necessity

Exponential growth in demand for computing power



Complex global geopolitical context

US AI chips export control tightened :
a push for homegrown hardware
→ Jan. 25, restrictions for 17 EU countries
→ Sept. 25, GAIN AI Act initiative

US rules on access to strategic European data:
Sovereign cloud and giga data centres initiatives
→ Eurostack Initiative
→ 1 GW data centres

Europe of defence
strategic independence is a must
→ Dramatic increase of EU defence spending targets: from 2% to 5% of GDP
→ Canada's deal strengthening European defence & security partnership

AI, a global fierce race that Europe can't afford to lose:
major public funding announcements
→ Invest AI (EU): €200bn / France: €109bn
→ EuroHPC JU AI factories program: AION Consortium

Hardware security:
Suspicions regarding
kill switch & backdoor

Europe hosts 148 of the Global Top 500 supercomputers⁽¹⁾

5 machines in the TOP10,
including 3 EuroHPC ones

TOP500
Nov. 2025



No4: JUPITER Booster – Germany

(1 exaflop)  EuroHPC Joint Undertaking

No6: HPC6 – Italy

(478 petaflops)

No8: Alps – Switzerland

(435 petaflops)

No9: LUMI – Finland

(380 petaflops)  EuroHPC Joint Undertaking

No10: LEONARDO – Italy

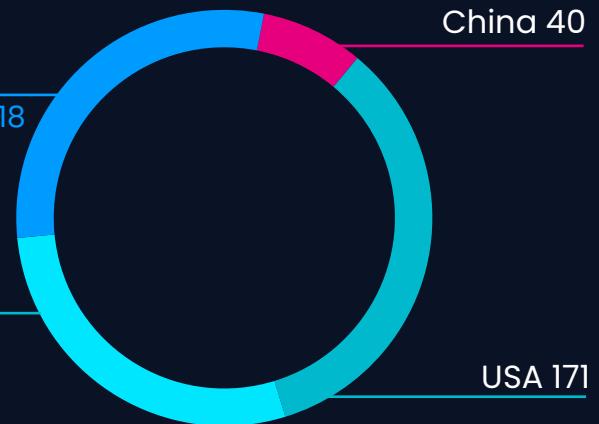
(241 petaflops)  EuroHPC Joint Undertaking

A great demand
for supercomputers in Europe

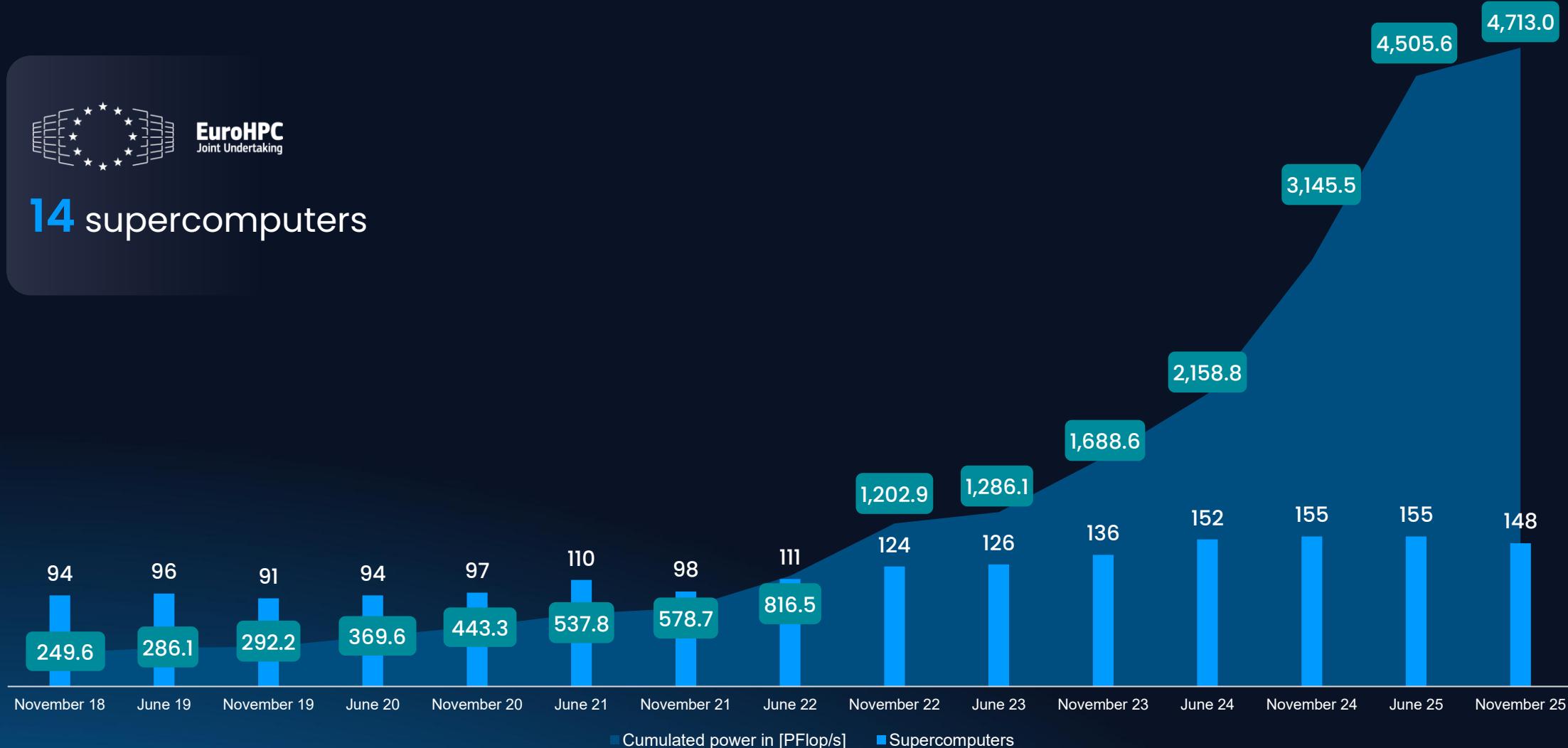
Since the launch of EuroHPC in 2018
+ 54 European supercomputers in the TOP500⁽¹⁾

Europe 148
vs 94 in November 2018

Rest of the world 141
incl. 43 in Japan

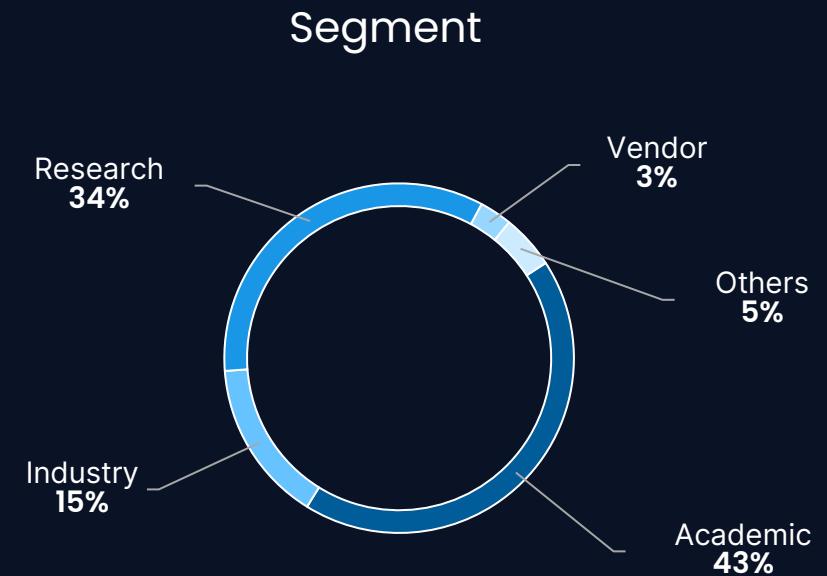
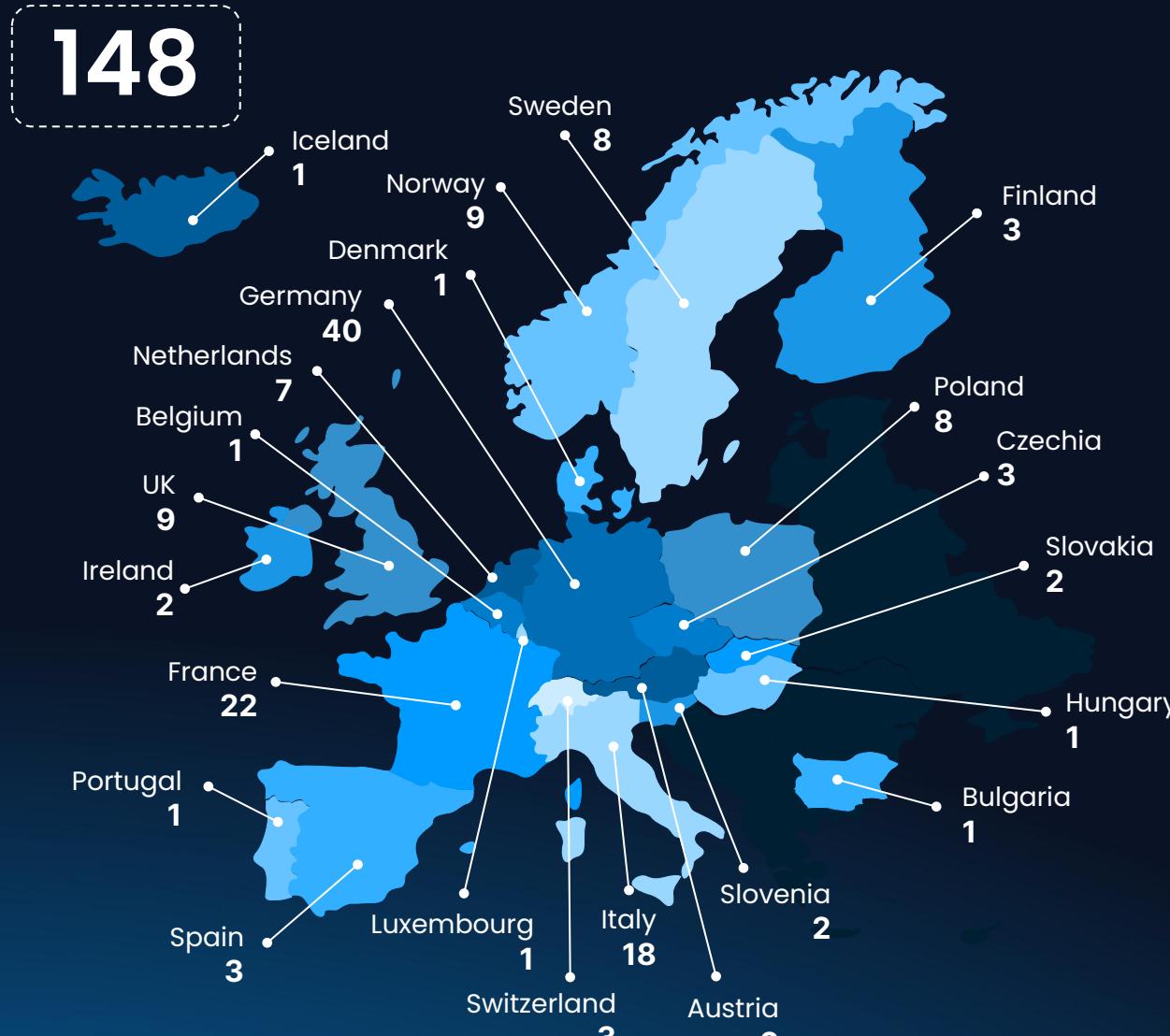


Europe supercomputers: a strong growth since EuroHPC launch⁽¹⁾



Germany, France & Italy lead the race⁽¹⁾

TOP500
Nov. 2025



But Europe is lagging behind in terms of hardware

1/3

of global supercomputing resources are consumed by Europe⁽¹⁾.



1/20

of global supercomputing equipment are supplied by Europe⁽¹⁾.



0%

of the processors powering Europe supercomputers are European⁽²⁾.



(1) Hyperion Research - HPC server and supercomputer market – 2024

(2) Sources: 66th edition of the global Top 500 most powerful supercomputers - November 2025 – Europe's supercomputers without Russia's: 148 / 52nd edition of the global Top 500 most powerful supercomputers - November 2018 – Europe's supercomputers without Russia's: 94

(3) Others: NEC (1.4%) / IBM (0.7%) / Fujitsu (0.7%)

Our answer: Strengthening Europe sovereignty (1/2)

with our processors

TODAY A dramatic situation for Europe

A complex
geopolitical context

Europe's strategic data
are processed by
non-European processors.

Huge impact on:

- Security
- Safety
- Competitiveness
- Sovereignty

Europe's supercomputers

Europe's data centres, AI, cloud & defence

TOMORROW
SiPearl's processors will fuel:



EuroHPC
Joint Undertaking

Our answer: strengthening Europe sovereignty (2/2)

with a fabless strategy

THE QUESTION

An advanced semiconductor factory in Europe?

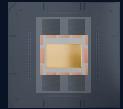
- Area of an airport ✓
- Cost: 20 to \$40Bn ?
- Energy demand ✓
- Pure water use ✗
- Highly skilled workforce ✗
- Investments in new equipment ✗
every 1.5 year: ASML machine = +\$300m
- Full-capacity operation ✗



RHEA V1

The most complex processor
ever designed in Europe

SILICON
PROCESS 6 nm
TSMC N6



LOGIC DIE IN ADVANCED SILICON PROCESS

80 arm®
Neoverse V1
cores⁽¹⁾

+61 billion
transistors

7.8 billion
equ. gates

16 metal
layers

6 PCIe x16
Gen5

2 PCIe x4
Gen5

4 DDR5
interfaces

80 MB
distributed
SLC

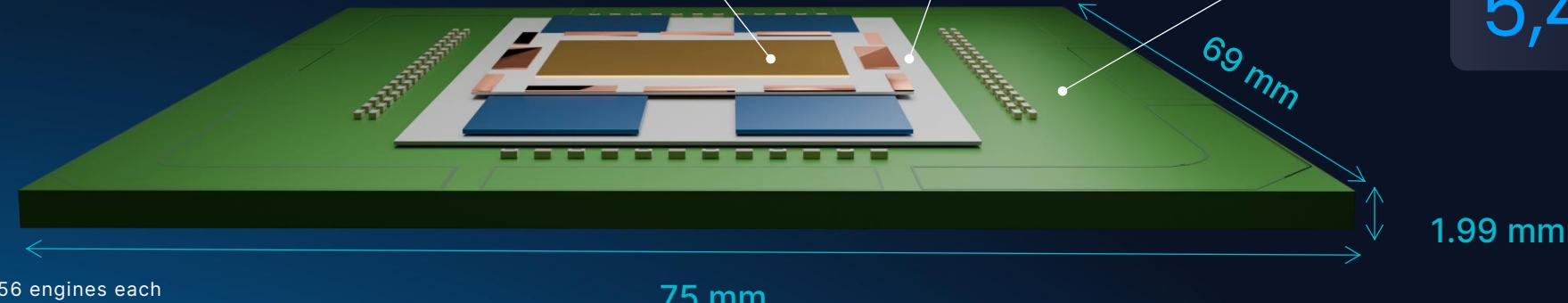
SILICON INTERPOSER

2.5D advanced
packaging

4 HBM
4x16=64GB

PACKAGE

5,430 pins



Our 2 first processors

Athenal capitalises on the work done for Rheal

RHEA 1
HPC & AI processor

In production / Sampling early 26

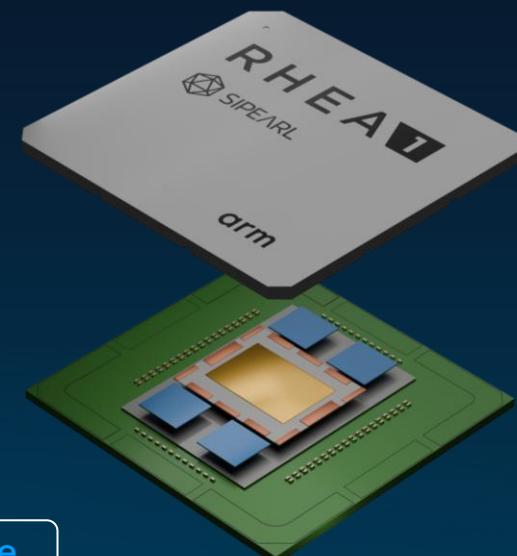
Silicon Process **6 nm**
TSMC N6

+61 billion transistors

4 HBM
 $4 \times 16 = 64\text{GB}$

80 arm® Neoverse V1 cores⁽¹⁾

4 DDR5 interfaces



Health Energy AI inference

(1) with 2x SVE256 engines each

ATHENA 1
Dual-use processor

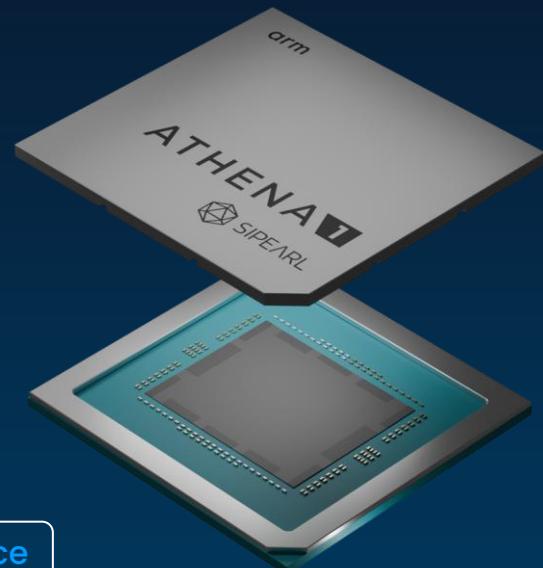
Under design / Release H2 27

A bespoke version of Rheal

European technology

SKUs of 16, 32, 48, 64 or 80
Arm Neoverse V1 cores

New packaging No HBM



Government Defence Aerospace

About... SiPearl

SiPearl is the European fabless designer of sovereign high-performance energy-efficient processors for HPC, AI and data centres. These processors will help address strategic dual-use challenges in the fields of security, defence, medical research, energy, climate and engineering with a reduced environmental footprint.

SiPearl completed the design of the most complex processor ever designed in Europe, Rhea1. Featuring 80 Arm Neoverse V1 cores, with 61 billion transistors, it is currently in production at TSMC and will be available for sampling in early 2026. Sipearl processors will equip both European exascale supercomputers owned by EuroHPC JU: Rhea1 will be integrated into the JUPITER machine based in Germany and Rhea2 will be part of Alice Recoque which will be located in France.

Incubated within the European Processor Initiative (EPI) consortium and seed-funded by the European Union, SiPearl employs 200 people in France, Spain, and Italy. Following a €130 million Series A, the company is launching its Series B round.



Funded by
the European Union



European
Processor
Initiative